

## **SPEC SHEET**

## J01A250W105K18T













J





250



105



18



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(1) Company title

Company title	
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SHANGHAI JEMLEAD

(6) Capacitance

Code	Capacitance Range
105	1.0uF

(2) Product

Product Code	
01	MULTILAYER CHIP CAPACITOR

(7) Capacitance Tolerance

Code	Tolerance	
K	±10%	

(3) AEC-Q200

Code	AEC-Q
Α	YES

(8) Chip Size

Code	Length*Width
18	3.2 * 1.6

(4) Rated Voltage

Code	Rated Voltage(Vdc)
250	25

(9) Tapping

Code	Туре
Т	PAPER TAPE/REEL

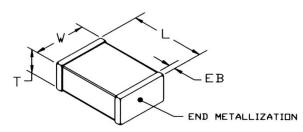
(5) Temperature Characteristics

Code	Temperature	Temperature
Code	Characteristics	Range
W	X7R	-55℃ to +125 ℃

\*Supplement

Test Parameters	
1 kHz ±50 Hz	
@ 1.0 VRMS, 25°C	

#### **Dimensions And Structure**



**Mechanical Characteristics** 

LENGTH "L"	WIDTH"W"	THICKNESS"T"	ENDBAND"EB"
3.2±0.3	1.6±0.3	1.6±0.2	0.6±0.3

Unit: mm



# SPEC SHEET

### **Electrical Characteristics**

Temperature coefficient	±15% (-55°C TO +125°C)
Dissipation Factor	< 5% @ 1kHz, 25°C
Insulation Resistance	100 Ohm-Farad OR10 G-Ohms, whichever is less @ WVDC, 25°C ( @ 125°C IR is 10% of 25°C requirement)
Dielectric Strength	2.5 X WVDC, 50 mA max

### **Electrical Characteristics**

PARAMETER	NP0		X7R		X5R	
TEMPERATURE COEFFICIENT:	0± 30 ppm/°C	-55 to +125°C	± 15%	-55 to +125°C	± 15%	-55 to +85°C
COLITICIENT.	20% 0% -20% -40% -40% -40% -40% -40% -40% -40% -4	56°C 78°C 100°C 125°C	20% 0% -20% -40% -40% -40% -50% -50% -50% -50% -50% -50% -50% -5	50°C 70°C 100°C 120°C	20% 0% 20% 40% 40% 40% 40% 40% 40% 40% 40% 40% 4	50°C 75°C 100°C 125°C
DISSIPATION FACTOR:	.001 (0.1%) max		WVDC ≥ 50 VDC, DF = 2.5% max WVDC = 25 VDC, DF = 3.0% max WVDC = 16 VDC, DF = 3.5% max		For Vrated ≥ 50 VDC, DF = 5% max For Vrated ≤ 25 VDC: DF = 10% max	
AGING:	None		2.5% / decade hour		2.5 % / decade hour	
INSULATION RESISTANCE:	1000ΩF or 100GΩ whichever is less @ 25°C, WVDC		$500\Omega F$ or $50G\Omega$ whichever is less @ 25°C, WVDC		100ΩF or 10GΩ whichever is less @ 25°C, WVDC	
DIELECTRIC STRENGTH:	For Vrated = 6 - 200 VDC, DWV = 2.5 X WVDC, 25°C, 50mA max.  For Vrated = 201 - 499 VDC, DWV = 2.0 X WVDC, 25°C, 50mA max.  For Vrated = 500 - 999 VDC, DWV = 1.5 X WVDC, 25°C, 50mA max.  For Vrated = 1000+ VDC, DWV = 1.2 X WVDC, 25°C, 50mA max.					
TEST PARAMETERS:	C > 100 pF; 1kHz ±50Hz;1.0±0.2 VRMS C ≤ 100 pF 1Mhz ±50kHz; 1.0±0.2 VRMS		1kHz ±50Hz;1.0±0.2 VRMS		1kHz ±50Hz; 0.5±0.2 VRMS	
NOTES:	Tanceram IR = 100 $\Omega$ F or 10 G $\Omega$ Tanceram DF for Vrated $\geq$ 50 VDC = 5% max. Tanceram DF for Vrated $\leq$ 25 VDC, DF = 10% max					